

ABSTRACT:

An arrangement is disclosed for receiving a digital signal from a transmission medium. The arrangement comprises a variable equalizer (6) for equalizing a received signal so as to obtain an equalized signal, and a bit-detector (12) for detecting a sequence of bits from the equalized signal. The arrangement further comprises an asynchronous sampling unit (4) for sampling a received analog signal so as to obtain a first signal having asynchronous samples.

To control the variable equalizer (16, 18, 20) a control signal is generated from the values of at least one sample of a signal having asynchronous samples, at either side of a zero crossing in said signal.

Fig. 1

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